



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Matthew J. Adiletta et al. Art Unit:
Serial No.: 10/776,702 Examiner:
Filed : February 10, 2004
Assignee : Intel Corporation
Title : SRAM CONTROLLER FOR PARALLEL PROCESSOR ARCHITECTURE
INCLUDING AN ADDRESS AND COMMAND QUEUE AND METHOD
FOR CONTROLLING ACCESS TO A RAM

Mail Stop Amendment

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

Copies of the U.S. patents/publications are not being provided per the waiver posted on the PTO website.

This filing is being made before the receipt of a first Office action on the merits. No fee is required.

The documents are in the English language; hence no concise explanation is necessary per Rule 98(a)(3).

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

May 11, 2005

Date of Deposit

Signature

Jennifer H. Payne

Typed or Printed Name of Person Signing Certificate

Applicant : Matthew J. Adiletta et al.

Attorney's Docket No.:10559-078003

Serial No.: 10/776,702

Filed : February 10, 2004

Page 2 of 2

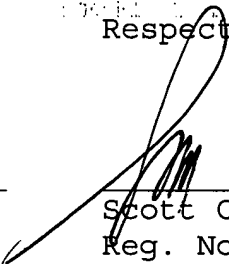


Consideration of the foregoing and enclosures plus the
return of a copy of the enclosed form PTO-1449 with the
Examiner's initials in the left column per MPEP 609 are
earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit
Account No. 06-1050.

Respectfully submitted,

Date: May 11, 2005



Scott C. Harris

Reg. No. 32,030

Attorney for Intel Corporation

Fish & Richardson P.C.
USPTO Customer No. **20985**
12390 El Camino Real
San Diego, CA 92130
Telephone: (858) 678-5070
Facsimile: (858) 678-5099

10511536.doc

Substitute Form PTO-1449

U.S. Department of Commerce
Patent and Trademark Office

Attorney's Docket No.

10559-078003

Application No.

10/776,702

**Information Disclosure Statement
by Applicant**

(Use several sheets if necessary)

Applicant

Matthew J. Adiletta et al.

Filing Date

February 10, 2004

Group Art Unit

(37 CFR §1.984)

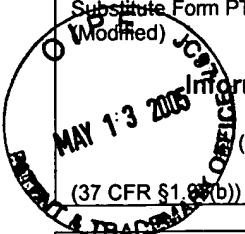
U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	4,016,548	04/1977	Law, et al.			
	AB	4,032,899	06/1977	Jenny, et al.			
	AC	4,075,691	02/1978	Davis, et al.			
	AD	4,788,640	11/1988	Hansen			
	AE	4,831,358	05/1989	Ferrio, et al.			
	AF	4,991,112	02/1991	Callemyn			
	AG	5,115,507	05/1992	Callemyn			
	AH	5,568,476	10/1996	Sherer, et al.			
	AI	5,701,434	12/1997	Nakagawa			
	AJ	5,740,402	04/1998	Bratt, et al.			
	AK	5,764,915	06/1998	Heimsoth, et al.			
	AL	5,784,712	07/1998	Byers, et al.			
	AM	5,850,530	12/1998	Chen, et al.			
	AN	5,918,235	06/1999	Kirshenbaum, et al.			
	AO	5,995,513	11/1999	Harrand, et al.			
	AP	6,061,710	05/2000	Eickemeyer, et al.			
	AQ	6,085,215	07/2000	Ramakrishnan, et al.			
	AR	6,111,886	08/2000	Stewart			
	AS	6,170,051	01/2001	Dowling			
	AT	6,212,542	04/2001	Kahle, et al.			
	AU	6,223,274	04/2001	Catthoor, et al.			
	AV	6,286,083	09/2001	Chin, et al.			
	AW	6,338,078	01/2002	Chang, et al.			
	AX	6,366,998	04/2002	Mohamed			
	AY	6,453,404	09/2002	Bereznyi, et al.			
	AZ	6,463,072	10/2002	Wolrich, et al.			
	AAA	6,463,480	10/2002	Kikuchi, et al.			

Examiner Signature

Date Considered

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Substitute Form PTO-1449 (Modified)  Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR § 1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-078003	Application No. 10/776,702
	Applicant Matthew J. Adiletta et al.		
	Filing Date February 10, 2004	Group Art Unit	

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	ABB	6,463,527	10/2002	Vishkin			
	ACC	6,466,898	10/2002	Chan			
	ADD	6,529,983	03/2003	Marshall, et al.			
	AEE	6,535,878	03/2003	Guedalia, et al.			
	AFF	6,584,522	06/2003	Wolrich, et al.			
	AGG	6,587,906	07/2003	Wolrich, et al.			
	AHH	6,604,125	08/2003	Belkin			
	AII	6,606,704	08/2003	Adiletta, et al.			
	AJJ	6,625,654	09/2003	Wolrich, et al.			
	AKK	6,661,794	12/2003	Wolrich, et al.			
	ALL	6,668,317	12/2003	Bernstein, et al.			
	AMM	6,671,827	12/2003	Guilford, et al.			
	ANN	6,792,488	09/2004	Wolrich, et al.			

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AOO							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	APP	"10-/100-Mbps Ethernet Media Access Controller (MAC) Core", NEC, 1998, pages 1-5.
	AQQ	"Nomadic Threads: A migrating multithread approach to remote memory accesses in multiprocessors", by Jenks, S.; Gaudiot, J.L. (abstract only) Publication Date: 20-23 Oct. 1996.
	ARR	"Overview of the START (*T) multithreaded computer" by Beckerie, M.J. (abstract only) Publication Date: 22-26 Feb. 1993.
	ASS	A. Ippoliti, et al., "Parallel Media Access Controller for Packet Communications at Gb/s Rates", 1990, IEEE, pages 991-996.
	ATT	Howard Frazier, "Gigabit Ethernet: From 100 to 1,000 Mbps", 1999, IEEE Internet Computing, pages 24-31.
	AUU	Howard Frazier, "The 802.3z Gigabit Ethernet Standard", 1998, IEEE Network, pages 6-7.

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	